

Sundance Multiprocessor Technology

embedded signal processing solutions

Communications, Wireless & Satellite

November 2011



EVP6472: Multicore DSP (Twelve C64x+ processor cores)



SMT8121: Multichannel UWB (6U cPCI/PXI Software defined)



MIMO 4x4 Developer's Board (LTE 2.4GHz and 5GHz ISM)

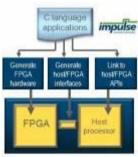


Dual MIMO WiMAX Channels (WiMAX 2.5GHz)



Electronic System Level Tools Aim at Sundance

Compile C code for Sundance FPGA Platforms



Impulse C allows users of Sundance FPGA platforms to generate <u>optimized VHDL or Verilog from C code</u> and create FPGA software/hardware applications for easier and more efficient FPGA implementation.

SUNDANCE

Visit <u>ImpulseAccelerated.com</u> or contact <u>Impulse</u> directly to find out how your C applications may be accelerated on an FPGA.

Board support packages and C-based signal processing libraries are available for the Sundance <u>SMT105 PCIe/104</u> and <u>EVP6472</u> solutions.

Updates from SEED:

XDS560 Emulators now Win64 Compatible To download the drivers, visit <u>XDS560.com</u>

XDS560v2: the New cost-effective solution for all DSP kits



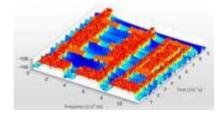
The new <u>SEED-XDS560v2</u> is based on the XDS560v2 system trace reference design from Texas Instruments. The XDS560v2 design is the next-generation of high-performance XDS560-class technology.

The SEED-XDS560v2 utilizes **USB2.0** (480Mb/s) and **Ethernet RJ45** (10/100Mb/s) ports for either local or remote debugging. It is supported by the latest

or remote debugging. It is supported by the latest **Code Composer Studio** versions (CCS4.x and CCS5.x), and is suitable for all the TI-based DSP systems including DM816x and C66xx.

Zoom in on LTE applications

LTE-Advanced Features in LTE PHY Lab v2



Sundance's partner IS-Wireless announces the availability of the first **LTE-Advanced baseband** reference library for <u>MATLAB LTE</u> <u>PHY Lab v.2</u>.

The product is released as a MATLAB Toolbox and brings new 3GPP Release 9 and 10 functionalities to the already wellestablished LTE PHY Lab v.1 implementing

3GPP Release 8.

LTE PHY Labs can be used at all stages of the software, hardware and IPR development, from research, prototyping and implementation, up to system benchmarking, verification and testing.

The LTE PHY Lab v2 is suitable to model LTE-Advanced algorithms on the dedicated Sundance MIMO Wireless development platforms: <u>MIMO LTE 4x4</u> while the sibling <u>WiMAX PHY Lab</u> on the <u>MIMO WiMAX platform</u>.

Series-7: 3U PXI Express | Series-1: PCI/104-Express | Series-C6472: C6472 Multicore DSP Rapid Development Systems | FPGA developer's Kits University Program | Contact us

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