

Sundance Multiprocessor Technology Limited

Product Specification

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Product Specification for SMT942

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Table of Contents

1	Introduction.....	5
2	Related Documents	5
2.1	Referenced Documents	5
2.2	Applicable Documents.....	6
3	Acronyms, Abbreviations and Definitions	6
3.1	Acronyms and Abbreviations	6
3.2	Definitions	6
4	Functional Description.....	6
4.1	Block Diagram	6
4.2	Module Description	6
4.3	Interface Description.....	7
4.3.1	Mechanical Interface	7
4.3.2	Electrical Interface.....	7
5	Verification Procedures	7
6	Review Procedures.....	7
7	Validation Procedures.....	7
8	Timing Diagrams.....	7
9	Circuit Description / Diagrams.....	7
10	Footprint.....	8
10.1	Top View	8
10.2	Bottom View.....	9
11	Pinout	10
12	Support Packages	10
13	Physical Properties	10
14	Safety	10
15	EMC	10

Table of Figures

Figure 1 - Block diagram.6

Figure 2 - Layout - Top Layer.8

Figure 3 - Layout - Bottom Layer.9



1 Introduction

The *SMT942* is a single width expansion TIM that plugs onto an [SLB](#) base module, the [SMT351T](#) (Virtex-5 LXT or SXT FPGA) as an example and incorporates 2 Texas Instrument dual-channel Digital-to-Analog Converters ([DAC5688](#)). The *SMT942* implements a comprehensive clock circuitry based on a chip ([CDCE72010](#)) from Texas instrument that allows synchronisation among the converters and the use of an external reference clock or sampling clock. It provides a complete conversion solution and stands as a platform that can be part of a transmit/receive base station. The *SMT942* has an on-board VCXO of frequency 245.76MHz.

DACs have a resolution of 16 bits and are able to update outputs at up to 800MHz. All converters are 1.8/3.3-Volt.

The [Xilinx FPGA](#) (Virtex-5 LXT or SXT series in the case of the *SMT351T*) on the base module is responsible for handling data coming from one of the following sources: Comports or **R**ocket **S**erial **L**ink ([RSL](#)). These interfaces are compatible with a wide range of Sundance's modules.

The memory (DDR2) on the base module (still based on the case where using an *SMT351T* SLB base module) can store outgoing samples.

Converter configuration, sampling and transferring modes are set via internal control registers stored inside the FPGA and accessible via Comport.

The *SMT942* module is well-suited for multi-carrier, wide bandwidth communication applications.

2 Related Documents

2.1 Referenced Documents

DAC datasheet: [Texas Instrument DAC5686](#).

Clock datasheet: [Texas Instrument CDCE72010](#).

2.2 Applicable Documents

3 Acronyms, Abbreviations and Definitions

3.1 Acronyms and Abbreviations

3.2 Definitions

4 Functional Description

4.1 Block Diagram

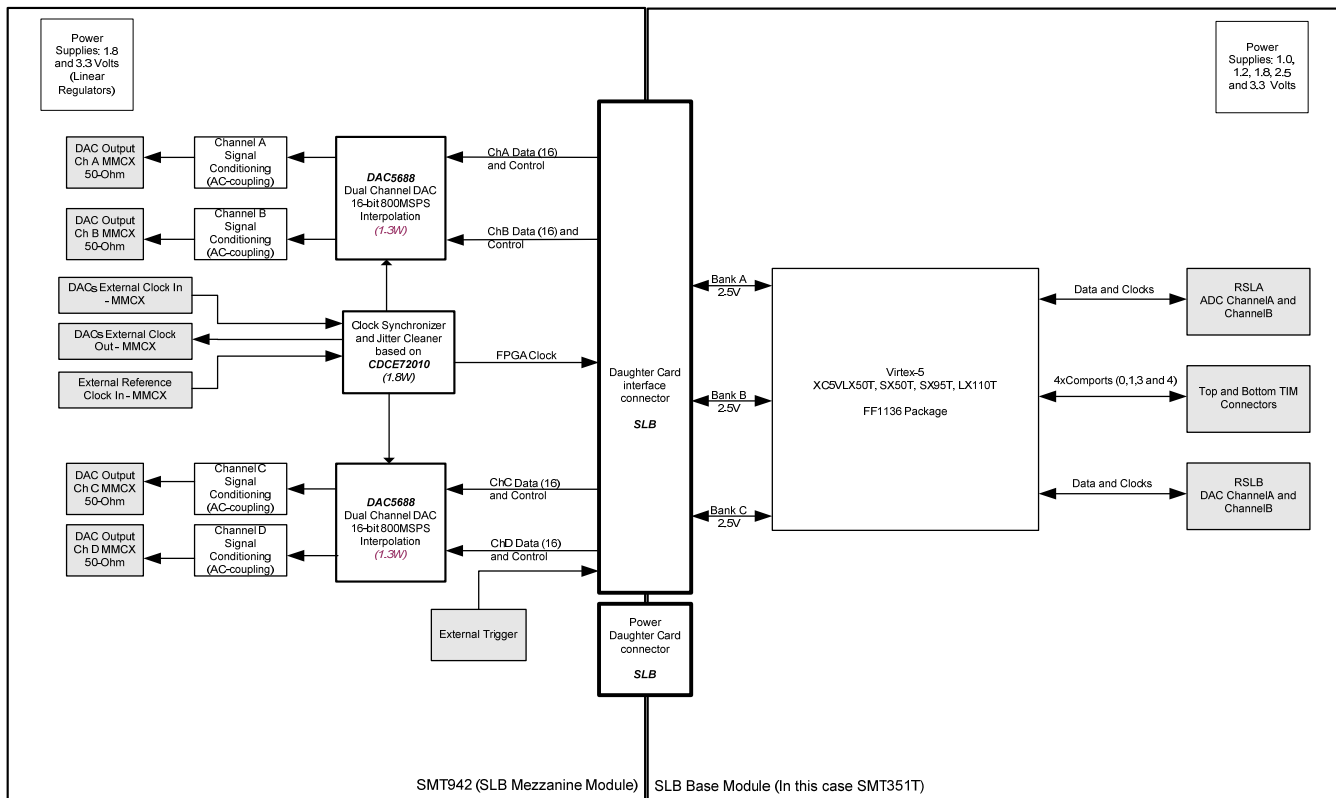


Figure 1 - Block diagram.

4.2 Module Description

The dual channel DAC ([DAC5688](#) – Texas Instrument) has integrated 2x-8x interpolation filters, a fine frequency mixer with a 32-bit complex numerically controlled oscillator, an on-board clock multiplier, an IQ compensation and an internal voltage reference.

4.3 Interface Description

4.3.1 Mechanical Interface

4.3.2 Electrical Interface

5 Verification Procedures

6 Review Procedures

7 Validation Procedures

8 Timing Diagrams

9 Circuit Description / Diagrams

10 Footprint

10.1 Top View

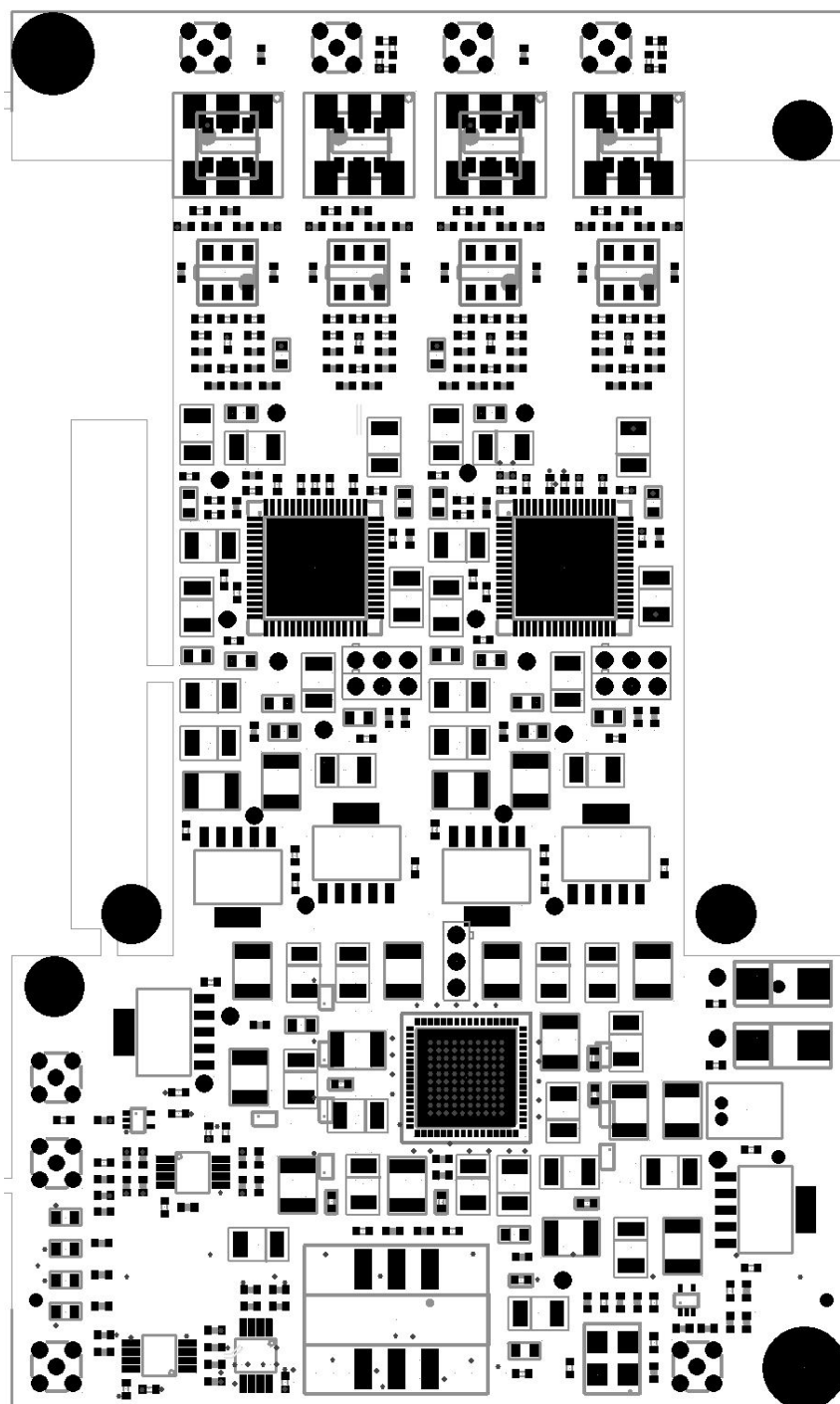


Figure 2 - Layout - Top Layer.

10.2 Bottom View

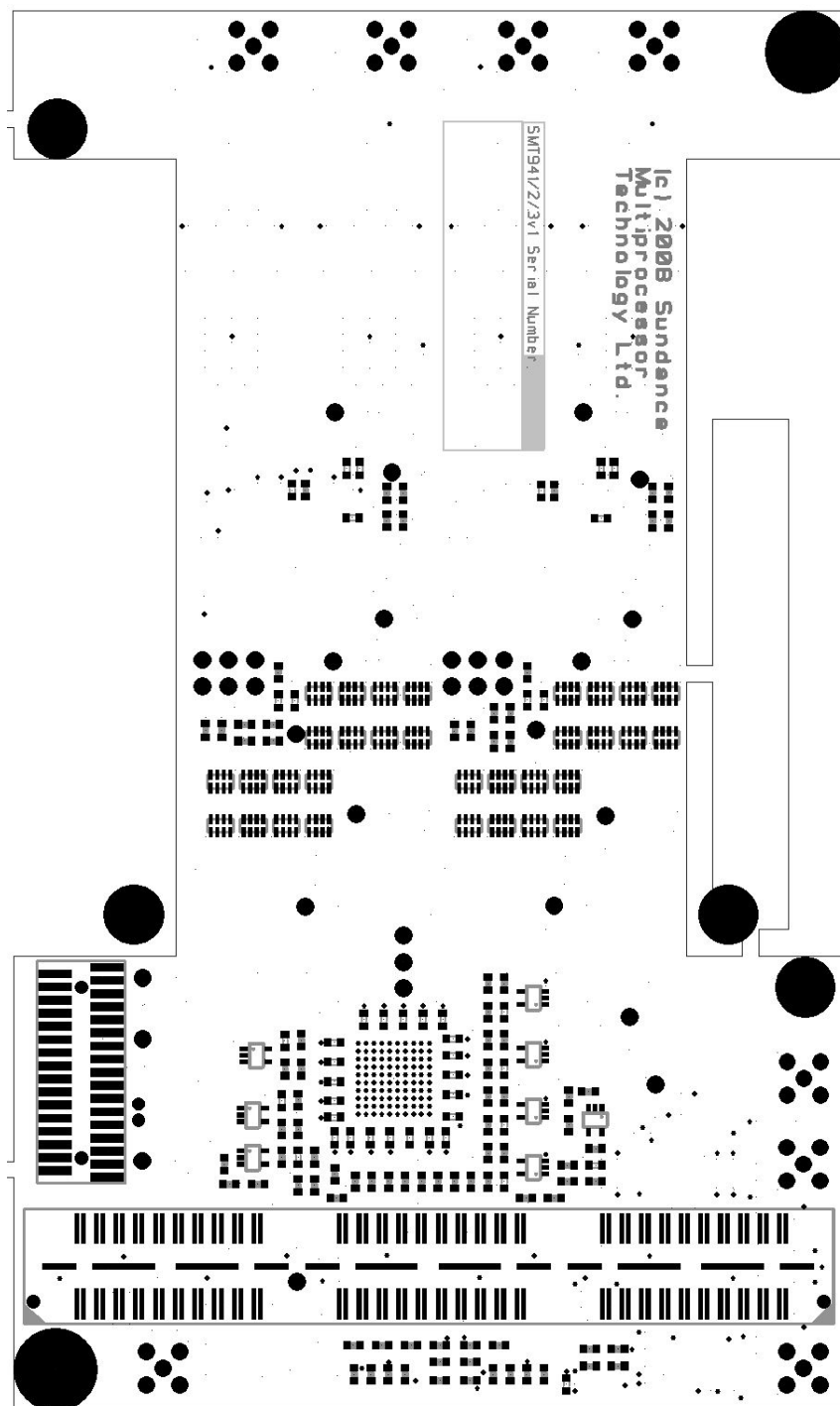


Figure 3 - Layout - Bottom Layer.

11 Pinout

12 Support Packages

13 Physical Properties

Dimensions		
Weight		
Supply Voltages		
Supply Current	+12V	
	+5V	
	+3.3V	
	-5V	
	-12V	
MTBF		

14 Safety

This module presents no hazard to the user when in normal use.

15 EMC

This module is designed to operate from within an enclosed host system, which is build to provide EMC shielding. Operation within the EU EMC guidelines is not guaranteed unless it is installed within an adequate host system.

This module is protected from damage by fast voltage transients originating from outside the host system which may be introduced through the output cables.

Short circuiting any output to ground does not cause the host PC system to lock up or reboot.