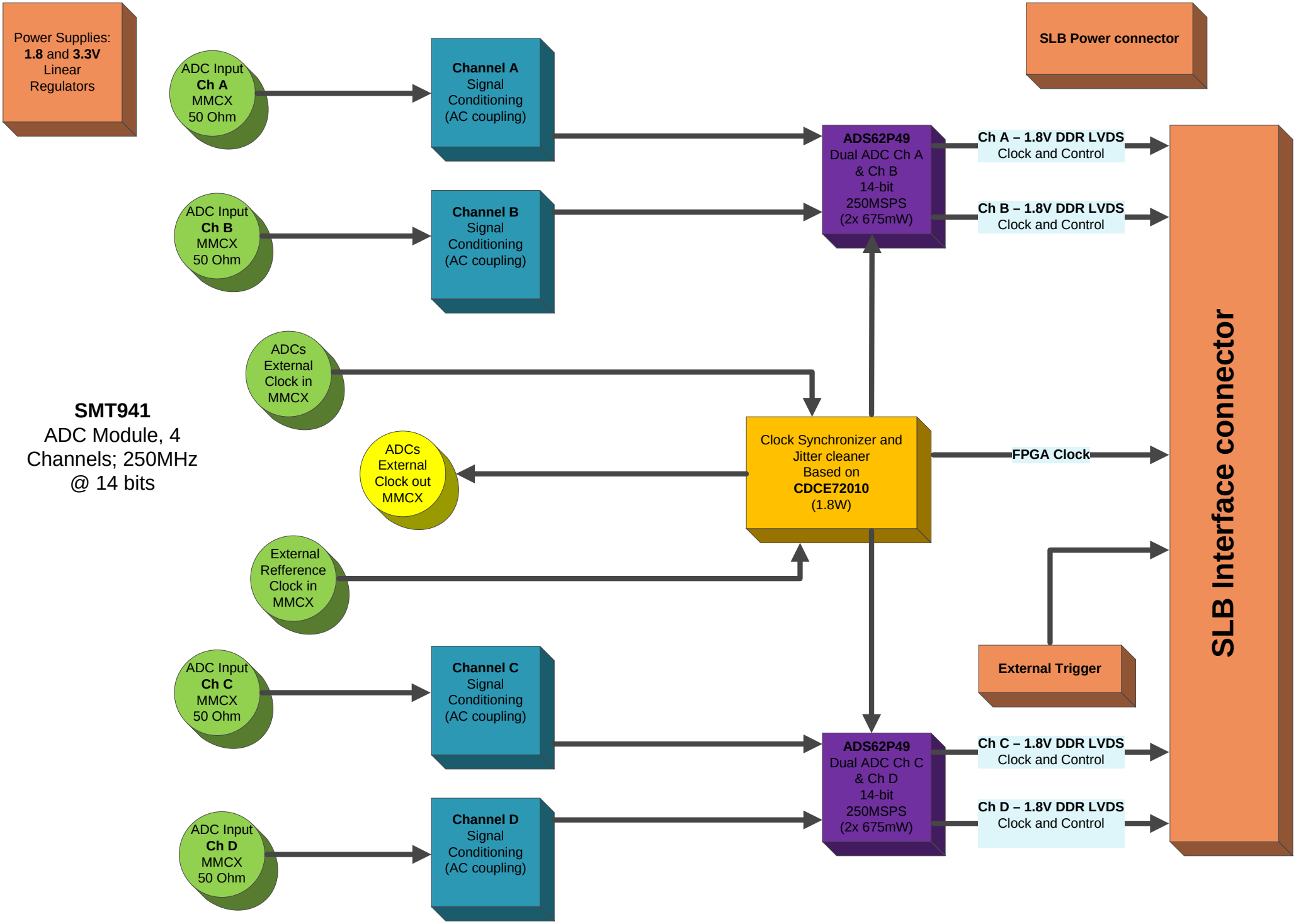
Power Supplies:
1.8 and 3.3V
Linear
Regulators
SLB Power connector
ADC Input
Ch A
MMCX
50 Ohm
Channel A
Signal
Conditioning
(AC coupling)
ADS62P49
Dual ADC Ch A
& Ch B
14-bit
250MSPS
(2x 675mW)
Ch A – 1.8V DDR LVDS
Clock and Control
Ch B – 1.8V DDR LVDS
Clock and Control
ADC Input
Ch B
MMCX
50 Ohm
Channel B
Signal
Conditioning
(AC coupling)
ADCs
External
Clock in
MMCX
SMT941
ADC Module, 4
Channels; 250MHz
@ 14 bits
ADCs
External
Clock out
MMCX
Clock Synchronizer and
Jitter cleaner
Based on
CDCE72010
(1.8W)
FPGA Clock
SLB Interface connector
External
Refference
Clock in
MMCX
External Trigger
ADC Input
Ch C
MMCX
50 Ohm
Channel C
Signal
Conditioning
(AC coupling)
ADS62P49
Dual ADC Ch C
& Ch D
14-bit
250MSPS
(2x 675mW)
Ch C – 1.8V DDR LVDS
Clock and Control
Ch D – 1.8V DDR LVDS
Clock and Control
ADC Input
Ch D
MMCX
50 Ohm
Channel D
Signal
Conditioning
(AC coupling)