EMC² Update - Summer 2016



EMC²-Development Platform



The wait is over...

.. as <u>Sundance</u> has now completed task WP4.6.3 and can ship the <u>EMC²-Development Platform</u> with a Xilinx Zynq Z7030 FPGA and a commercial version of Xilinx's Vivado add-on, the <u>SDSoC environment</u> that will allow Software Engineers to program a FPGA in 'C' without ever have to think about any VHDL constraints. Read more in this <u>article</u>. The commercial value of this bundle is €2370.00 and the cost to EMC²-Partners is only €750.00

A saving of almost 70%.



The Processor that powers...

.. the EMC²-Development Platform is a 'System-on-Module' concept to change between various Xilinx FPGAs, like a <u>Artix-7</u> or a <u>Kintex UltraScale</u> for 'SoftCore" implementation of multiple CPU or a <u>Xilinx Zynq</u> variation with Dual ARM9 and still spare FPGA fabric. The EMC²-family benefits significantly from the PC/104 stackable concept that allows multiple boards to be integrated into a multi-processing system, using PCI Express or Ethernet for inter-connection between each board in the system for a heterogeneous and asymmetrical computing platform

Contact Flemming from Sundance today!



UTIA will be using the EMC²-DP for implementing our "Asymmetric Multiprocessing" that combines a number of the 32-bit and 8-bit "SoftCore CPUs" on the re-configurable fabric logic of a Xilinx Zynq with integrated ARM9 "HardCore CPUs". We have ported the full solution to the SDSoC environment and a produced a demonstration of an Image Processing application and this was shown at <u>HiPEAC'16</u> in Prague.



SevenSol will be using the EMC²-DP as a base for the development of mixed criticality solutions capable of distributing data and time over Ethernet links. We explore determinism and redundancy for next generation of distributed smart-grid applications with full synchronization capabilities. We will support EMC²-DP with our range of FMC I/O Modules that are developed for the "White Rabbit Project"