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Multimedia and Video applications

Nowadays digital video applications are the interest of research teams aiming to define the future real time audio-visual applications.

The SMT339 is populated with a highly-integrated multimedia DSP (TMS320DM642). This fixed-point architecture is enhanced with the XC4VFX60 silicon device used as video accelerator and coprocessor. This subsystem, a masterpiece of integration particularly suits finely tuned DSP algorithms with a hardware/software design approach.

The GDD400 library will perform the best processing with optimized imaging and graphics functions for the C6000 and DM64x DSPs.

Camera Link, CMOS sensor, Firewire, Audio ports, fast Ethernet, and other user-defined I/O accessed through the Sundance LVDS Bus allow custom daughter-boards to share all this power. Control is made from the PCI interface through the SMT310 carrier.

More Details

Zoom in on IP cores by Sundance

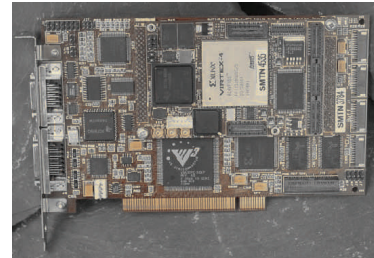
Sundance offers a range of ready to use IP cores for FPGA modules. These hardware cores are implemented to get the best performance of digital signal processing algorithms.

- VHDL designers can now use a proven FFT core based on a radix-32 architecture and having up to 1M points resolution.
The FFT core can be coupled to the polyphase filterbank core to process the data coming from an ADC such as the SMT391 in its interpolation mode, or the future 2GHz single ADC channel SMT386 (year 2007).
The imaging core can compress video flows in a performing JPEG format supporting theoretical speeds of 500 fps at a resolution 1280x1024.
Custom and off-the-shelf IP cores are also available from RF Engines Limited.

Our FPGA cores are as flexible as our modular development platforms.

More Details

November 2006

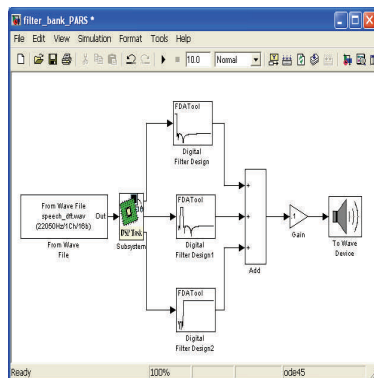


SMT8039 Real-Time Imaging System

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Parallel Application for Rapid Simulation (PARS)



Model-based design solution

PARS is the model-based design solution for systems utilizing parallel processing techniques and comprising DSPs and FPGAs.

Thanks to 3L Diamond, (the back end of PARS) users can now design and simulate complex systems within MATLAB/Simulink environment and then automatically generate target code for the combination of DSPs and FPGAs being used.

This advanced tool was originally developed under a US Navy contract and is being used for designing sophisticated SDR systems and hardware-in-the-loop applications.

Multi-DSP/FPGA systems like the SMT368, SMT395Q, SMT362 and fast DAQ boards are fully compliant. It increases the system level view and helps users to better benefit from the hardware flexibility and modularity offered by Sundance to design rapid prototyping solutions.

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