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When the FPGA meets DSP...

Sundance's DSP modules are composed of a hybrid architecture combining one Xilinx FPGA, and one or more TI DSPs to energize the computation power using the FPGA as DSP coprocessor. It significantly multiplies the flexibility of systems solutions for digital filtering, FFTs, DDCs, DUCs... and saves multiple DSP clock cycles used to compute pipelined instructions. 3L's [Diamond](#) integration tools maintains the simplicity of multi-processor systems with a common task methodology for both DSP and FPGA devices to run complex and high-performance signal processing algorithms.

Our first class quad 1GHz [DSP](#) module ([SMT395Q](#)) allies the VP70 FPGA coprocessor with four high-powered DSPs for an intensive computing solution.

[More Details](#)

Zoom in on MIMO-DS for digital communications

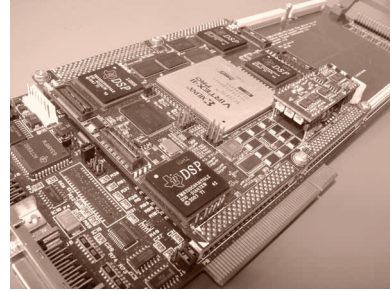
Is it possible to get solutions for multiple wireless communication standards onto a unique flexible, reconfigurable and scalable platform? Increasing the user data rates within a constrained frequency spectrum for wireless communication systems has been the subject of many years of research and development efforts. All conclusions seem to converge on MIMO (Multiple Inputs Multiple Outputs) technology...

Sundance's MIMO-Development System is a "smart" prototyping platform for the high-capacity wireless world. The Sundance MIMO-DS allows to prototype a range of communication standards for 3G up to the most demanding 4G-type of communications known as 802.11n. The Sundance solution is flexible, and it can be tailored to adapt to the users' requirements by using our range of signal processing modules.

We also would like to introduce [mimoOn](#). This young start-up company promotes the MIMO technology for professional wireless area networks and cellular networks... Having Sundance as a partner! Other R&D labs illustrates "[MIMO prototyping using Sundance's hardware and Software products](#)" as well.

[More Details](#)

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*SMT395Q,
intensive computing solution*

Newsletter Spotlight

- [FPGA coprocessor](#)
- [Sundance MIMO-DS](#)
- [Embedded PowerPC™](#)
- [JTAG Solution: SMT107](#)
- [Next Trade show: JAPAN](#)
- [Distributors](#)
- [Previous eNews](#)

Breathing life into the PowerPC in your embedded system



SMT398VP70: up to 2 PowerPCs

Sundance connects to the [SMT398VP70's](#) PowerPC™ 32-bit processor core in its firmware. Now customers can program embedded applications in their FPGA with [Xilinx EDK](#) combined with [SMT6400](#) software functions. Up to 2 embedded IBM PPC 405s can allow strong co-processing in the FPGA. The PowerPC 405 embedded-environment architecture adds even more flexibility to your design. Using the mixed DSP, FPGA and Embedded PowerPC combinations provide the "Price/Performance" solution for all aerospace and defence prototypes, wireless communications, multimedia broadcast and industrial control and supervision systems.

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