

PRESS RELEASE

09/07/2007

DIGITAL VIDEO OEMS TO BENEFIT FROM SCALEABLE INFRASTRUCTURE PLATFORM FROM SUNDANCE

SUNDANCE



Sundance Provides Digital Video Infrastructure Platform based on Texas Instruments' DSPs

London – England - July 9, 2007 – Responding to the need for higher bandwidth in high-definition video applications for broadcast, video surveillance and compression technologies, Sundance Multiprocessor Technology today announced the availability of its high-performance Digital Video Infrastructure Platform (DVIP). The platform offers original equipment manufacturers (OEM) a complete, integrated solution for digital video development.

Built on Sundance's modular and infinitely-scalable multiprocessing concept, the DVIP leverages the performance and flexibility of two Texas Instruments Incorporated (TI) TMS320C6455 digital signal processors (DSP) and a TMS320DM642 DSP-based digital media processor. The 1GHz C6455 DSPs allow multiple processors to be connected via a Serial Rapid I/O (SRIO) interface, a concept that is well suited to Sundance's modular hardware. Additionally, the infrastructure platform incorporates Xilinx Virtex-4 FX60 FPGAs with the capacity to implement IP-Cores from leading compression suppliers to perform real-time encoding and decoding.

The modular architecture of the DVIP allows OEM customers to increase the processing performance in the field and add one of more than 40 additional variant modules that support the Sundance TIM (Texas Instruments Module) standard. As this standard is open-domain, it is also possible to integrate proprietary solutions.

"TI is seeing an increasing demand for commercial-off-the-shelf OEM solutions that use our leading-edge digital multimedia DSPs, and Sundance's DVIP supports a range of demanding applications therein," said Andy Richardson, business development manager, TI. "The TMS320C6455 DSPs add a level of scalability to the DVIP that will make it a popular development environment for OEMs."

The interface to end equipment is handled by two PowerPC 405 general-purpose processors that can be found at the core of the Virtex-4 FPGA. The factory default options are high-speed USB 2.0 (480Mbits/sec), 1Gbit Ethernet, and Low-Voltage Differential Signals (LVDS) for display. RS485 and IP-Cores can be sourced for FireWire, SATA (hard disk interface), PCI Express, and other I/O standards or bespoke interfaces.

The DVIP is available now from a starting price of USD25000.00 in single quantities with OEM prices on request.

The DVIP is supported by comprehensive firmware and software-development tools from leading suppliers, including TI, Xilinx and MathWorks. Optionally, the use of Diamond tools from www.3l.com will kick-start any development and provide a leading-edge Eclipse-based IDE.

"The DVIP is an impressive platform and is perfect hardware for 3L's multiprocessor DSP and FPGA tools," said Dr. Peter Robertson, Managing Director of 3L Ltd. "Sundance has given us a perfect launch pad for the Diamond IDE"

About Sundance

Sundance leads the world in the design and manufacture of mixed COTS digital signal processing and FPGA architectures. Sundance modular and customizable COTS hardware and software systems are easy to reconfigure to suit all high-speed I/O and signal processing applications, enabling the rapid prototyping and development of embedded systems. Designers and manufacturers of wireless infrastructures, satellite communications, industrial signal processing, sonar/radar, software defined radio (SDR), multimedia and telecommunications, all depend on the flexibility of Sundance's mixed DSP+FPGA platforms and systems to get their products to market – FAST!

Key design and manufacturing facilities are located in the UK, and mainland Europe and our worldwide network of agents – extending as far as Asia – provides quality service and support.

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High-res image: <ftp://ftp2.sundance.com/pub/images/Newsletters/DVIP.tiff>