

PRESS RELEASE

06/11/2006



HD Digital Video, Telecom and IP OEMs to Benefit from New Scaleable Twin DSP and FPGA Based Development System

SUNDANCE

London – England

It is often said 'two heads are better than one' when it comes to solving problems. The new [SMT362](#) from Sundance Multiprocessor Technology takes this to heart. At its core are not one but two Texas Instruments TMS320C6455 DSPs and the long awaited Xilinx Virtex-4 FX60 FPGA. A scaleable, modular TIM design, with a huge range of connectivity options, makes the SMT362 the ideal development platform for original equipment manufacturers to design and deliver the next generation of HD based video infrastructure, telecom and IP devices.

With an operating frequency of 1GHz and 2MB on-chip level 2 cache each, the TMS320C6455 DSPs have the highest fixed-point processing power in the 'C6000 range. These, combined with the stunning Virtex-4 FPGA FX60, that boasts nearly 60,000 logic cells and 16 RocketIO serial transceivers, allow users of the SMT362 to gain breakthrough performance at the lowest cost.

It never takes long for OEMs to explore the processing power limits of a new board. This is not a problem for the SMT362 – it is designed to scale-up the DSP and FPGA capability by simply connecting with additional SMT362s or other TIMs. Massive raw processing power on tap combined with an almost limitless ability to scale-up, are compelling reasons for OEMs to use the SMT362 as a more productive alternative to ASICs and ASSPs. However, Sundance did not stop there; they have integrated 256MB of DDR2-500 SDRAM for each of the DSPs giving developers an even greater ability to implement complex DSP tasks.

DSP engineers developing the telecoms backbone to meet growing expectations and demand will benefit from the increased capacity and productivity the SMT362 has to offer. So too will the developers of equipment for heavy users of integrated audio/video in teleconferencing, high definition imaging in the medical world, HDTV broadcasting and so on – the very applications that place great demand on the telecommunications infrastructure.

Achieving 'fast time to market' is all important in the highly competitive and fast paced telecoms, broadcast and IP product arena. By taking care of the high-speed digital video hardware, Sundance have allowed designers to concentrate on

EDITORIAL ENQUIRIES

USA

Sundance DSP Inc.
Dr. Nory Nakhaee
4790 Caughlin Parkway 233, Reno,
NV 89509-0907, U.S.A.
Tel: (775) 827-3103
Fax: (775) 827-3664

email: Nory.N@sundance.com

MIDDLE, SOUTH, EAST EUROPE

Sundance Italia S.R.L.
Dr. Fabio Ancona
Corso XXV Aprile 55/3
16040 S. Salvatore di Cogorno (GE), Italy
Tel: +39 0185 385193
Fax: +39 0185 385370

email: Fabio.A@sundance.com

NORTH EUROPE & REST OF THE WORLD

Sundance Multiprocessor Technology Ltd.
Mr. Flemming Christensen
Chiltern House, Waterside, Chesham
Bucks, HP5 1PS, England
Tel: +44 (0)1494 793167
Fax: +44(0)1494 793168

email: Flemming.C@sundance.com

implementing their own algorithms using the two powerful DSPs and the cutting-edge FPGA.

Comprehensive software tools are provided to make algorithm implementation easy so OEMs can quick-start their development of advanced video infrastructures, HDTV, IP and telecoms technology. Software includes TI's Code Composer Studio Integrated Development Environment (IDE) and 3L's Diamond FPGA.

"When Alexander Graham Bell invented the telephone in 1876 and Edison the Kinetoscope in 1891, they could never have envisaged how the technologies would merge to become the audio visual experience without wires possible today. However, I am sure if they were with us now, they would love to have a piece of kit like the SMT362 to bring their applications alive in double-quick time," commented Sebastien Maury, DSP system analyst with Sundance Multiprocessor Technology.

The [SMT362](#) is the ideal DSP and FPGA platform for developers of digital video products and follows hot on the heels of the SMT339, its analogue-video cousin, launched just a few weeks before. Sundance are happy to advise on the selection of DSP and FPGA development platforms.

Designers and OEMs wanting technical specifications of the SMT362, a powerful, versatile COTS development platform, should visit www.sundance.com/smt362.

More information in UK:

Sebastien Maury, DSP System Analyst, Sundance Multiprocessor Technology Ltd
Chiltern House, Waterside, CHESHAM, Bucks HP5 1PS
Tel. +44 (0)1494 792421 Fax. +44 (0)1494 793168
E-mail: CIPR0451@sundance.com Web: www.sundance.com

More information in USA:

Dr Nory Nakhaee, Sundance Digital Signal Processing Inc.
4790 Caughlin Parkway #233, Reno, NV 89509-0907, USA
Tel. (775) 827 3103 Fax. (775) 827 3664
E-mail: CIPR0451@sundance.com Web: www.sundance.com

High res image can be downloaded from:

<http://www.clickintopr.com/editors/articleDetail.asp?pjID=451>

Acronyms

ASIC – application-specific integrated circuit

ASSP – application specific signal processor

COTS – commercial off the shelf

DSP – digital signal processing

FPGA – field programmable gate array

HDTV – high definition TV

IP – internet protocol

MIPS – million instructions per second

MPEG – moving picture experts group

NTSC – national transmission standards committee

PAL – phase alternation line

RSL – rocket serial link

SDRAM – synchronous dynamic random access memory

TIM – Texas Instruments module

ZBT – zero bus transfer