SMT709 Block Diagram

8 Configurable Video Inputs

IP Conditioning

SAA7111 8 Video Decoder

SYNCS
8-bit Video Data
I2C Interface

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IP Conditioning

SAA7111 8 Video Decoder

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I2C Interface

SLB Power connector

SLB Interface connector

DDR2 1Gbyte 220MHz

64 bit data
19 address
13 control

Custom Front Pannel

120 I/O Pins

Rear Card Connector

PXI Control (Trigger, Clock etc)

PXIe Connector 8 lanes of 2.5Gb/s

32 bit PCI 33/66 MHz (Option)

8x 2.5 Gb/s serial links

SHB (Option)

FPGA Virtex5 FF1136 Package

LEDs and Misc. I/O

4x 2.5 Gb/s serial links

RSL Connector 4 lanes of 2.5Gb/s

2x 2.5 Gb/s serial links

SATA Connectors 2 Lanes of RSL

2x 2.5 Gb/s serial links

SFF Connectors 2 Lanes of Fibre

Ethernet

Ethernet PHY

USB

Front Panel Connector
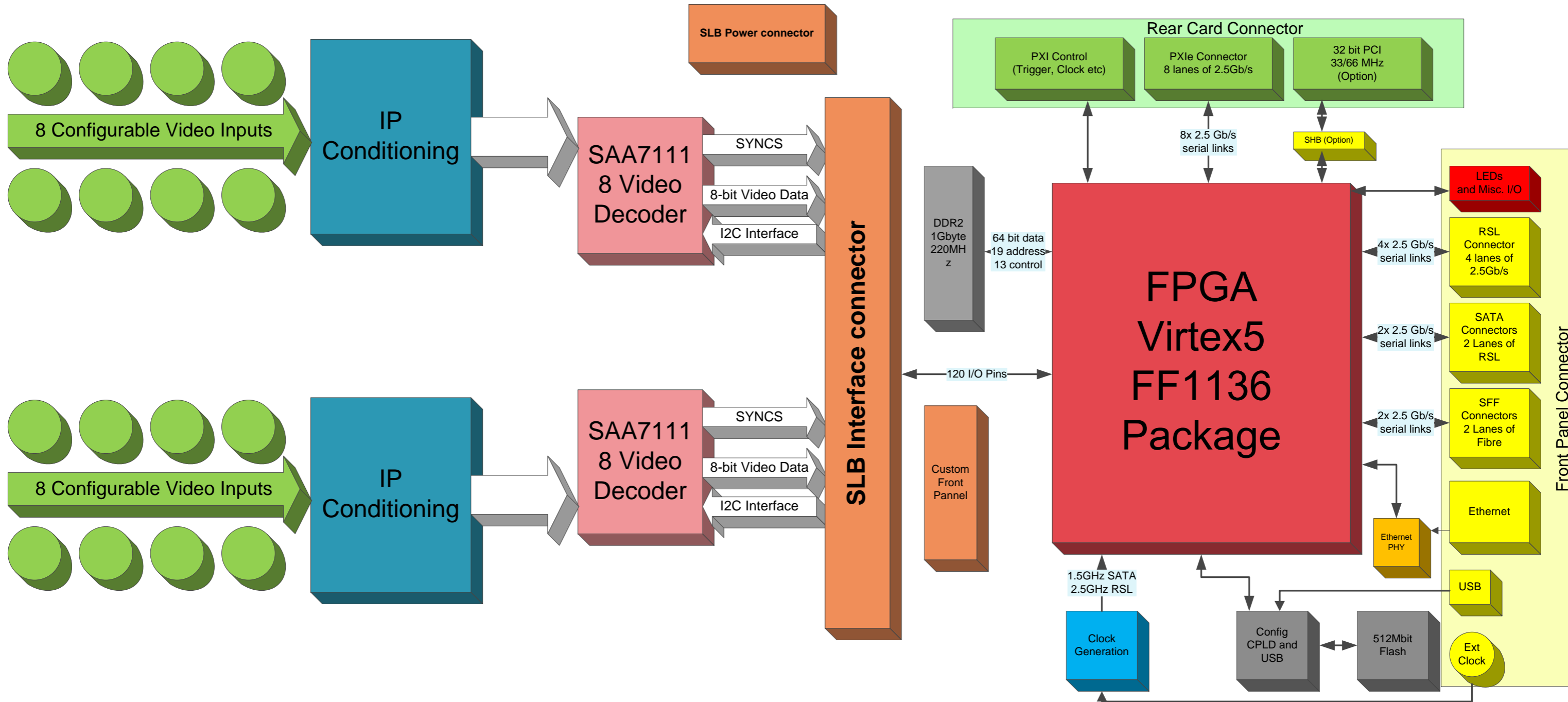
1.5GHz SATA
2.5GHz RSL

Clock Generation

Config CPLD and USB

512Mbit Flash

Ext Clock

SMT909 Block Diagram

SMT700 Block Diagram