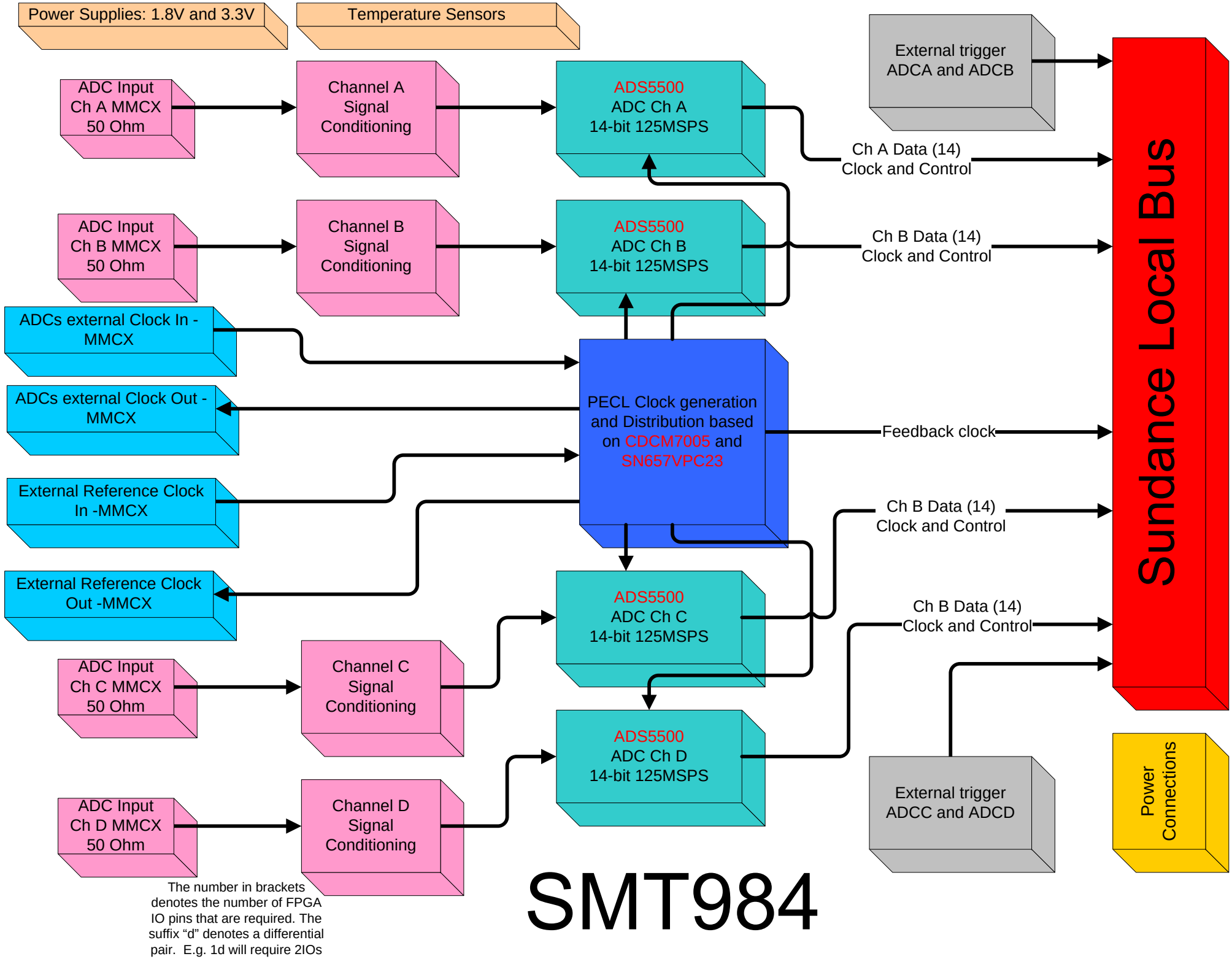
Power Supplies: 1.8V and 3.3V
Temperature Sensors
ADC Input Ch A MMCX 50 Ohm
Channel A Signal Conditioning
ADS5500 ADC Ch A 14-bit 125MSPS
External trigger ADCA and ADCB
Ch A Data (14) Clock and Control
ADC Input Ch B MMCX 50 Ohm
Channel B Signal Conditioning
ADS5500 ADC Ch B 14-bit 125MSPS
Ch B Data (14) Clock and Control
ADCs external Clock In - MMCX
ADCs external Clock Out - MMCX
PECL Clock generation and Distribution based on CDCM7005 and SN657VPC23
Feedback clock
External Reference Clock In -MMCX
Ch B Data (14) Clock and Control
External Reference Clock Out -MMCX
ADS5500 ADC Ch C 14-bit 125MSPS
Ch B Data (14) Clock and Control
ADC Input Ch C MMCX 50 Ohm
Channel C Signal Conditioning
ADS5500 ADC Ch D 14-bit 125MSPS
ADC Input Ch D MMCX 50 Ohm
Channel D Signal Conditioning
External trigger ADCC and ADCD
Sundance Local Bus
Power Connections
SMT984
The number in brackets denotes the number of FPGA IO pins that are required. The suffix “d” denotes a differential pair. E.g. 1d will require 2IOs