

SMT6042

User Manual



Certificate Number FM 55022

Revision History

Date	Comments	Engineer	Version
04/10/05	First release	JPA	1.0

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Introduction

The SMT6042 is a toolbox for Xilinx System Generator to design the FPGA on Sundance's modules.

The following modules are supported:

- SMT381-VP
- SMT391-VP

Installation

Unzip the package into <installdir>

Open Matlab and run <installdir>\startup.m

The documentation for each block is available by clicking the help button of the block.

Examples

Some example designs are provided to help you getting started.

The examples are available in the following directories:

- testSMT381 gathers the examples for the SMT381-VP
- testSMT391 gathers the examples for the SMT391-VP

Requirements

- 1 Matlab and Simulink R14 or above to able to run System Generator.
- 2 System Generator v7.1 or above
- 3 Xilinx ISE 7.1.3i