

FPGA Front Runner: FPGA Safety and Security

Thursday 21st November

09:30 - 14:00

Cambridge
+ Online



About This Event

Join us at this event where industry leaders will discuss the challenges of ensuring FPGA security and compliance with industry safety standards, covering everything from supply chain considerations to FPGA hardware and IP security.

Sundance's MD, Flemming Christensen, will give a talk on ***'How to secure supply of 'COTS' FPGA Modules'***

Find out about securing FPGAs beyond the bitstream, FPGA requirements tracking, and the importance of the Requirements Traceability Matrix. The event will also cover integrating DevSecOps strategies into FPGA development pipelines, the impacts of the EU Cyber Resilience Act on business and product design, and insights on securing the supply of COTS FPGA Modules and other key industry perspectives.

Where

The Cass Centre
Shaftesbury Road,
Cambridge, CB2 8BS

United Kingdom

When

Thursday November 21st

9:30 - 14:00 GMT

Event lasts 4 hours 30 minutes

Speakers

Flemming Christensen

Sundance

Tobias Adryan

Synopsys

Ian Pearson

Microchip

Espen Tallaksen

EmLogic

Andrew Swirski

Beetlebox

Register now

*This is a hybrid event: you can join in person or attend virtually

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