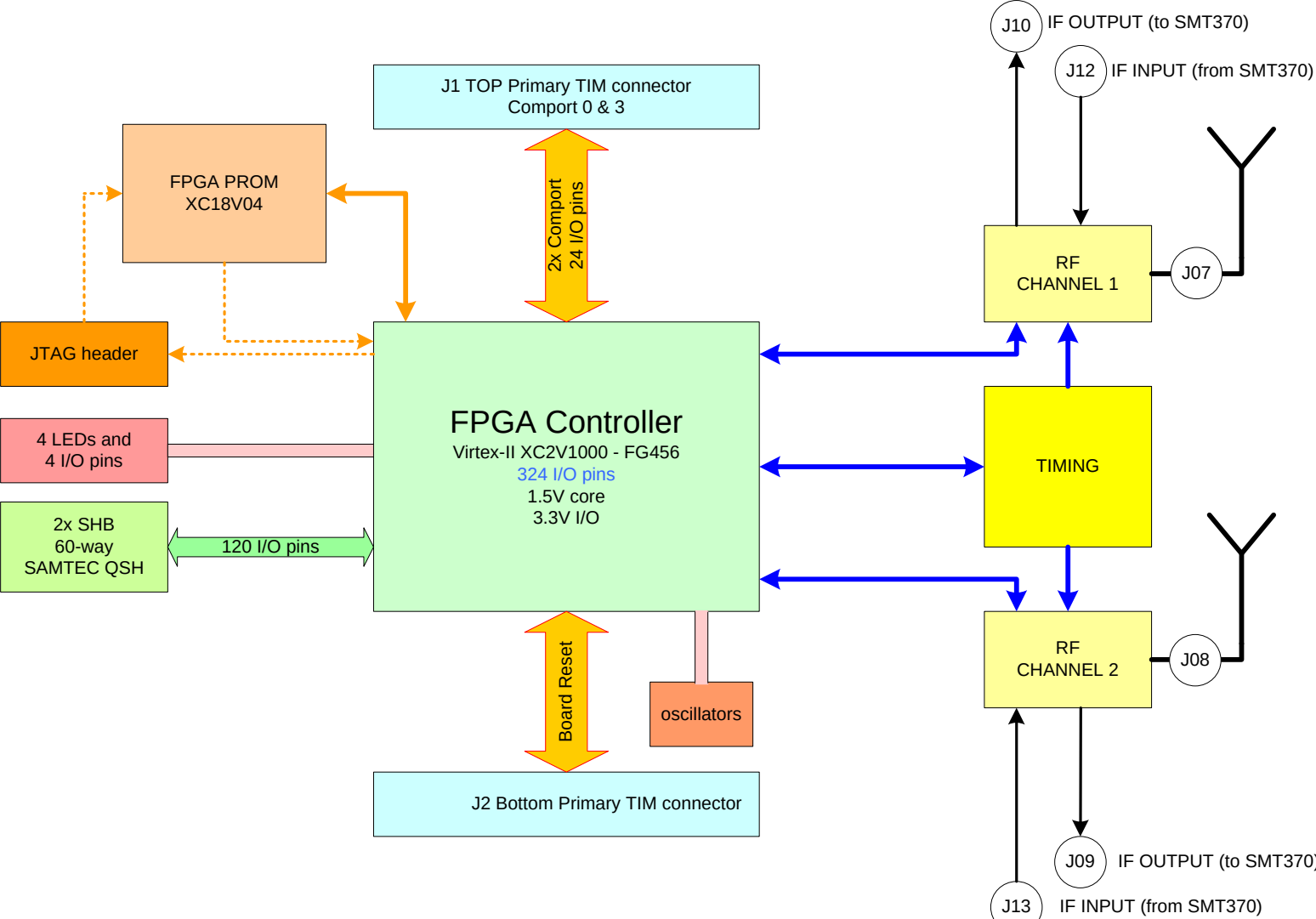
J10
IF OUTPUT (to SMT370)
J12
IF INPUT (from SMT370)
J1 TOP Primary TIM connector
Comport 0 & 3
FPGA PROM
XC18V04
2x Comport
24 I/O pins
RF
CHANNEL 1
J07
JTAG header
FPGA Controller
Virtex-II XC2V1000 - FG456
324 I/O pins
1.5V core
3.3V I/O
4 LEDs and
4 I/O pins
TIMING
2x SHB
60-way
SAMTEC QSH
120 I/O pins
RF
CHANNEL 2
J08
Board Reset
oscillators
J2 Bottom Primary TIM connector
J09
IF OUTPUT (to SMT370)
J13
IF INPUT (from SMT370)