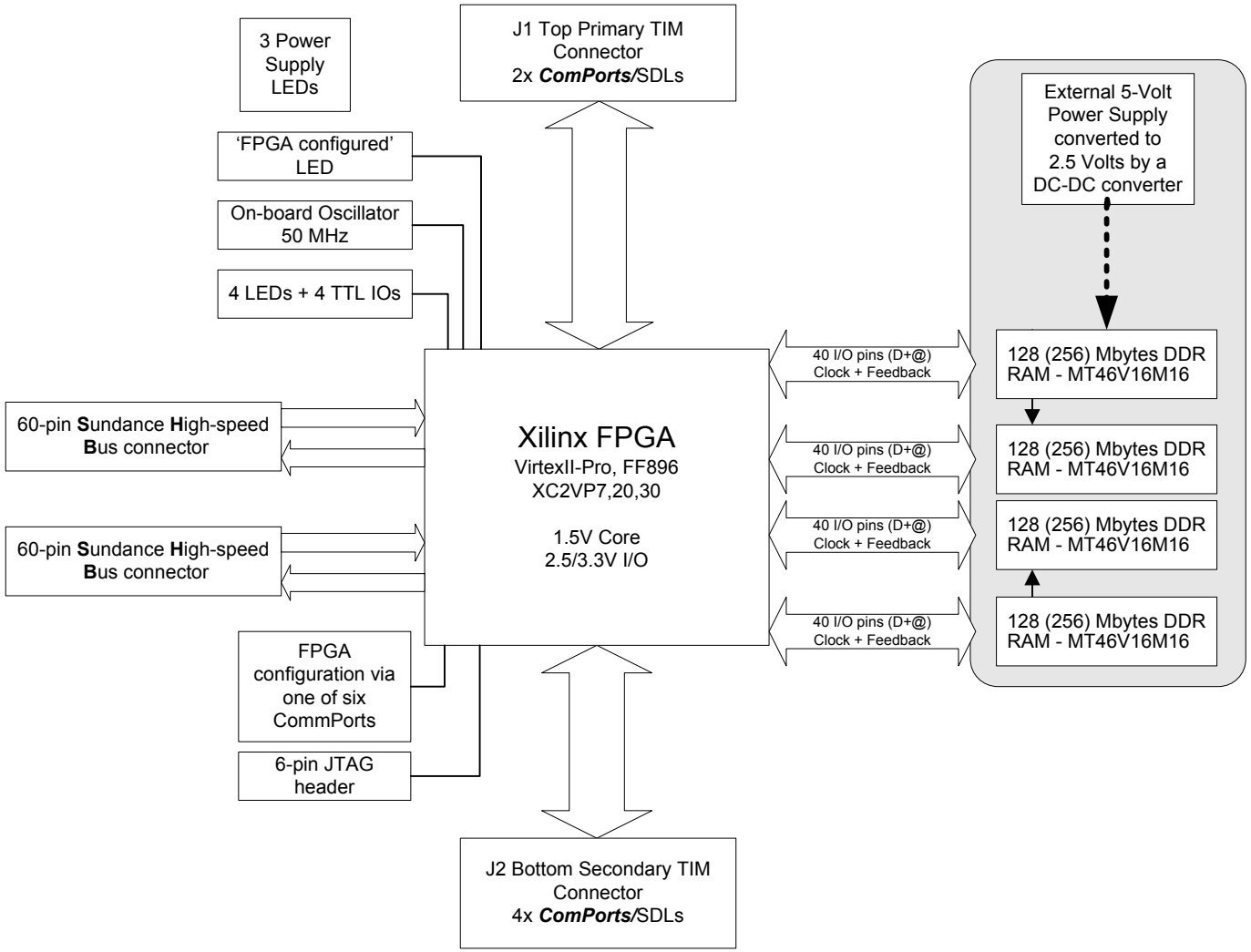
3 Power Supply LEDs
J1 Top Primary TIM Connector
2x ***ComPorts/***SDLs
'FPGA configured' LED
On-board Oscillator 50 MHz
4 LEDs + 4 TTL IOs
External 5-Volt Power Supply converted to 2.5 Volts by a DC-DC converter
60-pin **S**undance **H**igh-speed **B**us connector
Xilinx FPGA
VirtexII-Pro, FF896
XC2VP7,20,30
1.5V Core
2.5/3.3V I/O
40 I/O pins (D+@)
Clock + Feedback
128 (256) Mbytes DDR RAM - MT46V16M16
60-pin **S**undance **H**igh-speed **B**us connector
FPGA configuration via one of six CommPorts
6-pin JTAG header
J2 Bottom Secondary TIM Connector
4x ***ComPorts/***SDLs