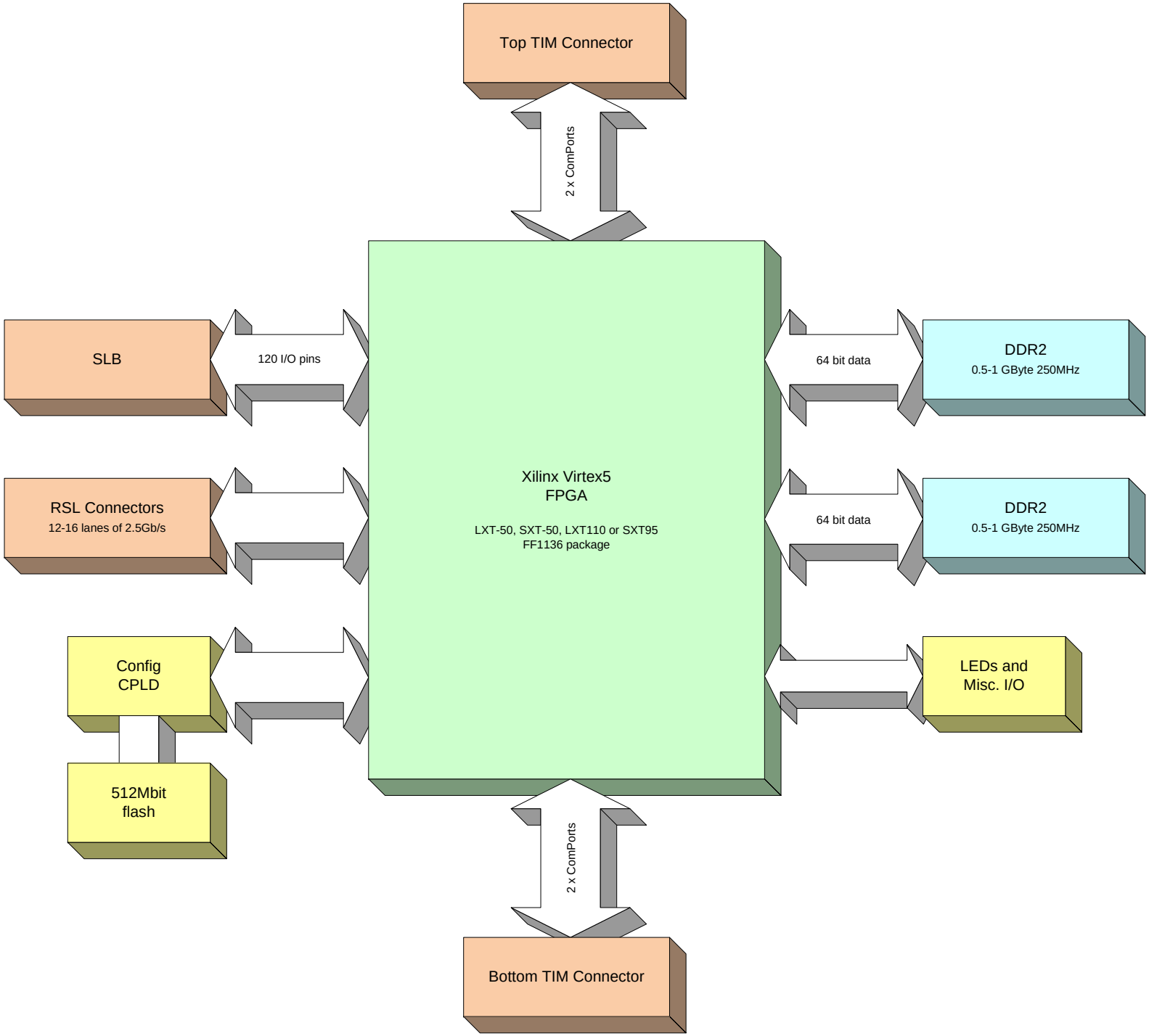
Top TIM Connector
2 x ComPorts
SLB
120 I/O pins
RSL Connectors
12-16 lanes of 2.5Gb/s
Config
CPLD
512Mbit
flash
Xilinx Virtex5
FPGA
LXT-50, SXT-50, LXT110 or SXT95
FF1136 package
64 bit data
DDR2
0.5-1 GByte 250MHz
64 bit data
DDR2
0.5-1 GByte 250MHz
LEDs and
Misc. I/O
2 x ComPorts
Bottom TIM Connector