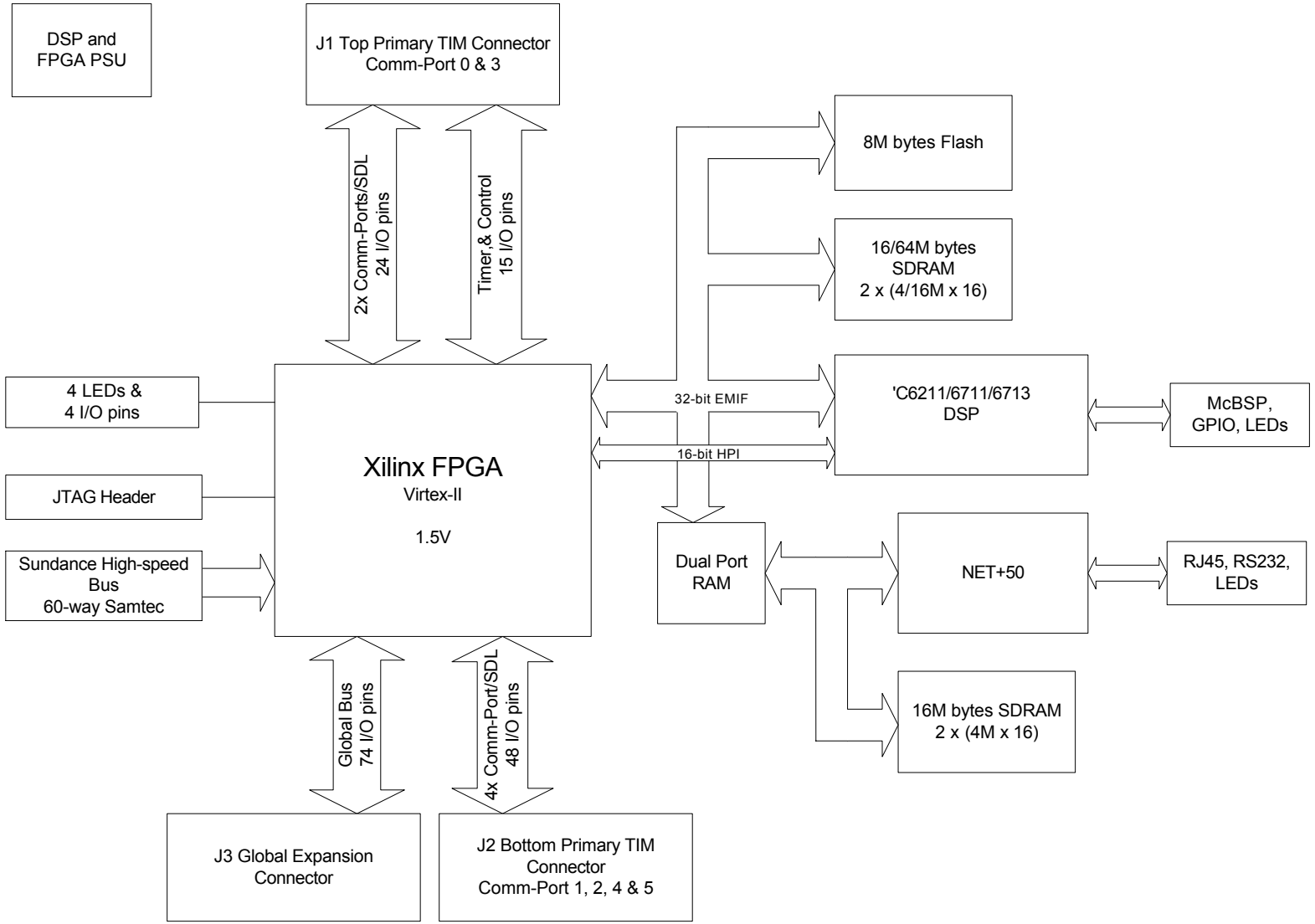
DSP and
FPGA PSU
J1 Top Primary TIM Connector
Comm-Port 0 & 3
2x Comm-Ports/SDL
24 I/O pins
Timer,& Control
15 I/O pins
8M bytes Flash
16/64M bytes
SDRAM
2 x (4/16M x 16)
4 LEDs &
4 I/O pins
JTAG Header
Sundance High-speed
Bus
60-way Samtec
Xilinx FPGA
Virtex-II
1.5V
32-bit EMIF
16-bit HPI
'C6211/6711/6713
DSP
McBSP,
GPIO, LEDs
Dual Port
RAM
NET+50
RJ45, RS232,
LEDs
16M bytes SDRAM
2 x (4M x 16)
Global Bus
74 I/O pins
4x Comm-Port/SDL
48 I/O pins
J3 Global Expansion
Connector
J2 Bottom Primary TIM
Connector
Comm-Port 1, 2, 4 & 5