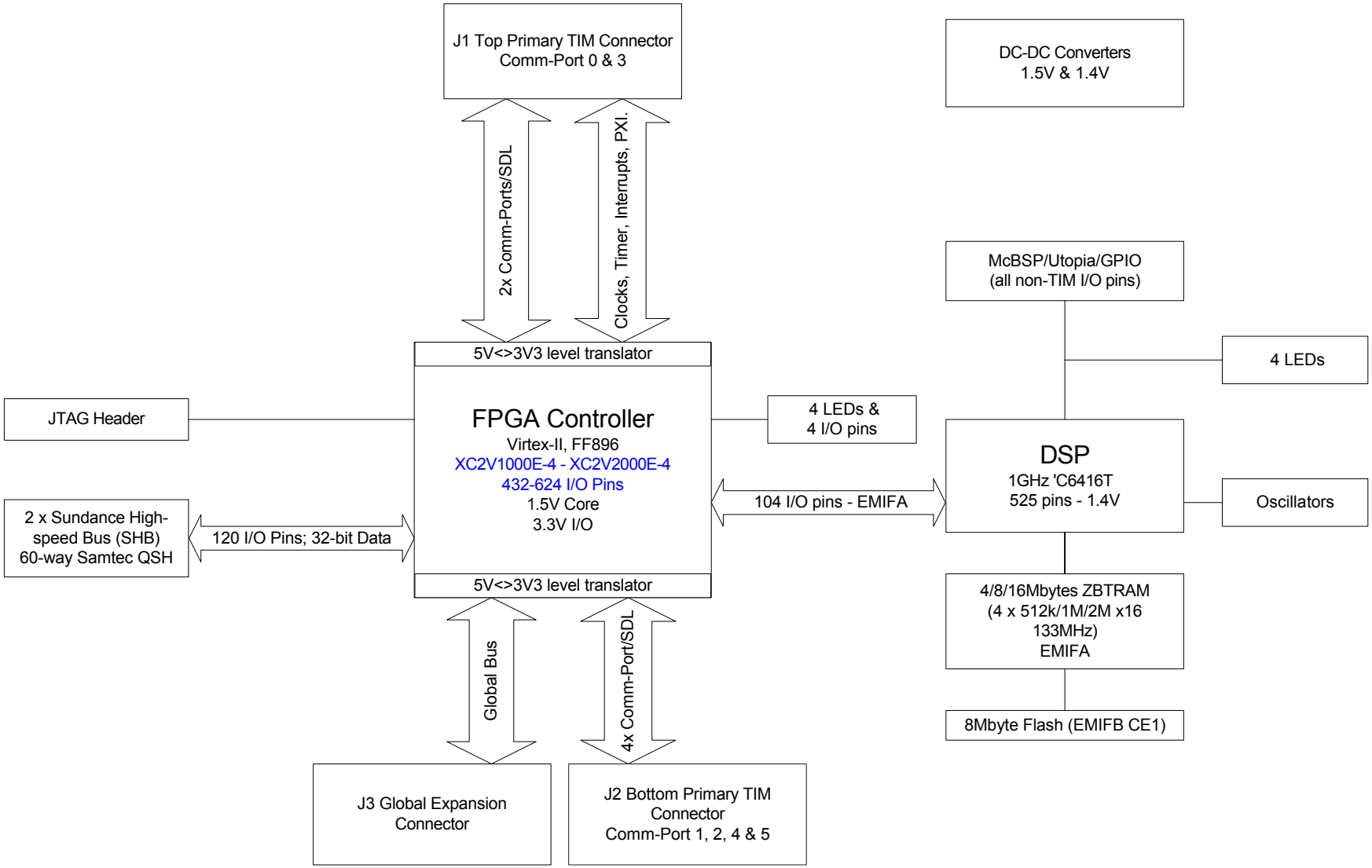
J1 Top Primary TIM Connector
Comm-Port 0 & 3
DC-DC Converters
1.5V & 1.4V
2x Comm-Ports/SDL
Clocks, Timer, Interrupts, PXI.
McBSP/Utopia/GPIO
(all non-TIM I/O pins)
4 LEDs
5V<>3V3 level translator
JTAG Header
FPGA Controller
Virtex-II, FF896
XC2V1000E-4 - XC2V2000E-4
432-624 I/O Pins
1.5V Core
3.3V I/O
4 LEDs &
4 I/O pins
DSP
1GHz 'C6416T
525 pins - 1.4V
Oscillators
2 x Sundance High-speed Bus (SHB)
60-way Samtec QSH
120 I/O Pins; 32-bit Data
104 I/O pins - EMIFA
5V<>3V3 level translator
4/8/16Mbytes ZBTRAM
(4 x 512k/1M/2M x16
133MHz)
EMIFA
Global Bus
4x Comm-Port/SDL
8Mbyte Flash (EMIFB CE1)
J3 Global Expansion
Connector
J2 Bottom Primary TIM
Connector
Comm-Port 1, 2, 4 & 5