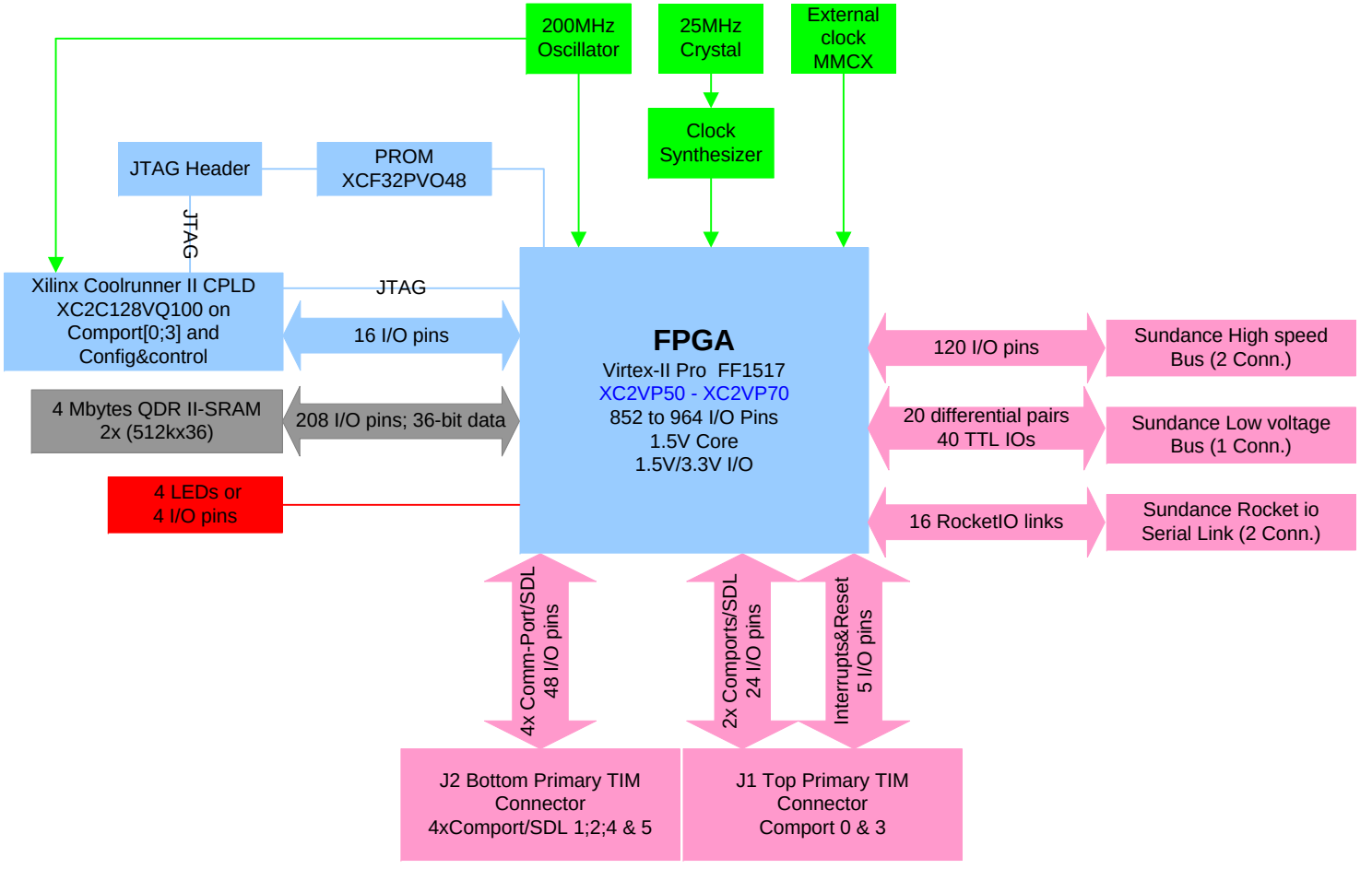
200MHz Oscillator
25MHz Crystal
External clock MMCX
Clock Synthesizer
JTAG Header
PROM XCF32PVO48
JTAG
Xilinx Coolrunner II CPLD XC2C128VQ100 on Comport[0;3] and Config&control
JTAG
16 I/O pins
FPGA
Virtex-II Pro FF1517
XC2VP50 - XC2VP70
852 to 964 I/O Pins
1.5V Core
1.5V/3.3V I/O
4 Mbytes QDR II-SRAM 2x (512kx36)
208 I/O pins; 36-bit data
4 LEDs or 4 I/O pins
120 I/O pins
Sundance High speed Bus (2 Conn.)
20 differential pairs 40 TTL IOs
Sundance Low voltage Bus (1 Conn.)
16 RocketIO links
Sundance Rocket io Serial Link (2 Conn.)
4x Comm-Port/SDL 48 I/O pins
2x Comports/SDL 24 I/O pins
Interrupts&Reset 5 I/O pins
J2 Bottom Primary TIM Connector 4xComport/SDL 1;2;4 & 5
J1 Top Primary TIM Connector Comport 0 & 3