

# SMT 7098 "Pipeline" MultiFPGA System

- Memory Cores
- I/O Pins - Samtec QSH Connector
- ComPort Core 20MHz @ 8-bit
- SDB Core 100MHz @ 32-bit
- Global Bus 50MHz @ 32-bit interface connected to SMT310Q Carrier board.
- JTAG Test Bus between all SMT398 Modules and then to a Xilinx JTAG cable
- User-defined I/O pins

