

Power Supplies:
1.8 and 3.3V
Linear
Regulators

SLB Power connector

DAC
Output
Ch A
MMCX
50 Ohm

Channel A
Signal
Conditioning
(AC coupling)

DAC5688
Dual channel
DAC
16-Bit
800MSPS
Interpolation
(1.3W)

Ch A – Data (16)
and Control

DAC
Output
Ch B
MMCX
50 Ohm

Channel B
Signal
Conditioning
(AC coupling)

Ch B – Data (16)
and control

ADCs
External
Clock in
MMCX

ADCs
External
Clock out
MMCX

Clock Synchronizer and
Jitter cleaner
Based on
CDCE72010
(1.8W)

FPGA Clock

External
Reference
Clock in
MMCX

DAC
Output
Ch C
MMCX
50 Ohm

Channel C
Signal
Conditioning
(AC coupling)

External Trigger

Ch C – Data (16)
and Control

DAC
Output
Ch D
MMCX
50 Ohm

Channel D
Signal
Conditioning
(AC coupling)

DAC5688
Dual channel
DAC
16-Bit
800MSPS
Interpolation
(1.3W)

Ch D – Data (16)
and control

SLB Interface connector

SMT942
DAC Module, 4
Channels; 800MHz
@ 16 bits