

Power Supplies:
1.8 and 3.3V
Linear
Regulators

SLB Power connector

ADC Input
Ch A
MMCX
50 Ohm

Channel A
Signal
Conditioning
(AC coupling)

ADC Input
Ch B
MMCX
50 Ohm

Channel B
Signal
Conditioning
(AC coupling)

ADS62P49
Dual ADC Ch A
& Ch B
14-bit
250MSPS
(2x 675mW)

Ch A – 1.8V DDR LVDS
Clock and Control

Ch B – 1.8V DDR LVDS
Clock and Control

SLB Interface connector

SMT943
ADC / DAC
Module, 2 ADC
Channels; 250MHz
@ 14 bits; 2 DACs
@ 800MHz/16 bits

ADCs
External
Clock in
MMCX

ADCs
External
Clock out
MMCX

Clock Synchronizer and
Jitter cleaner
Based on
CDCE72010
(1.8W)

FPGA Clock

External
Reference
Clock in
MMCX

External Trigger

DAC
Output
Ch A
MMCX
50 Ohm

Channel A
Signal
Conditioning
(AC coupling)

DAC
Output
Ch B
MMCX
50 Ohm

Channel B
Signal
Conditioning
(AC coupling)

DAC5688
Dual channel
DAC
16-Bit
800MSPS
Interpolation
(1.3W)

Ch A – Data (16)
and Control

Ch B – Data (16)
and control